

IN THE CLAIMS

Please cancel claims 1-20 without prejudice or disclaimer of the subject matter thereof.

Please add new claims 21-26 as follows:

21. A storage system comprising:  
a plurality of host adaptors connected to at least one host device, which form interfaces for the host device;  
a plurality storage devices for storing therein data transferred from the host device;  
a plurality of disk adaptors connected to said storage devices, which form interfaces for said storage devices;  
a cache for temporarily storing therein data transferred between said plurality of host adaptors and said plurality of disk adaptors; and  
two buses, connected to said plurality of host adaptors, said plurality of disk adaptors, and said cache, which transfer data among said plurality of host adaptors, said plurality of disk adaptors, and said cache, wherein said two buses operate as a pair of buses having a transfer ability larger than one of said two buses.

22. A storage system according to claim 21, further comprising:

a memory for storing a status of which of said two buses is being used.

Sub  
B1  
23. A storage system according to claim 22, wherein said memory can be referred to by an external processor.

Sub  
B2  
24. A storage system comprising:  
a plurality of host adaptors connected to at least one host device, which form interfaces for the host device;  
a plurality of storage devices for storing therein data transferred from the host device;  
a plurality of disk adaptors connected to said storage devices, which form interfaces for said storage devices;  
a plurality of caches for temporarily storing therein data transferred between said plurality of host adaptors and said plurality of disk adaptors; and  
two buses, connected to said plurality of host adaptors, said plurality of disk adaptors, and said plurality of caches, which transfer data among said plurality of host adaptors, said plurality of disk adaptors, and said plurality of caches, wherein said two buses operate as a pair of buses having a transfer ability larger than one of said two buses.

25. A storage system according to claim 24, further comprising:  
a memory for storing a status of which of said two buses is being used.